

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	Examiner: Rossoshek, Yelena
Kazuaki GOTO et al.)	Group Art Unit: 2825
Serial No. 10/811,835)	
Filed: March 30, 2004)	Confirmation No. 3051
For: METHOD OF DESIGNING A CIRCUIT)	Date: October 26, 2008
LAYOUT OF A SEMICONDUCTOR)	
DEVICE WITH A SPARE)	
UNDERGROUND CELL (as previously)	
amended))	

AMENDMENT UNDER 37 C.F.R. 1.111

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is responsive to the Office Action dated July 24, 2009 in the above application. In view of the following amendments and remarks, reconsideration of the application is respectfully requested.

Amendments to the Claims

The claims are amended as shown on the following pages under the heading "LIST OF CURRENT CLAIMS". The list shows the status of all claims presently in the application including any current amendments. This list of claims is intended to supersede all prior listing(s) of the claims in the application. Any cancellation of claims is made without prejudice or disclaimer.